

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10056193	01/24/2002	365	63	2818	V. Nguyen

**APPLICANTS: Khatri Dirgha; Cox Dail; Johnson Christopher;

2818

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no
Verified and Acknowledged Examiner's initials		ATTORNEY DOCKET NO 2008.003800
TITLE : Memory module with integrated bus termination		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.
TERMINAL		Print Fig.	
DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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